

IN THE CLAIMS

Please amend the claims as follows:

1-16. (Cancelled).

17. (Currently Amended) An electronic circuit comprising conversion means for converting an input voltage into an output voltage, said conversion means comprising:

at least a first energy storage means and a second energy
5 storage means; and

switching means for periodically coupling, under control
of clock signals, said at least first and second energy storage
means to one another ~~under the control of clock signals~~ so as to
store energy in the at least first and second energy storage means,
10 and for transferring at least a portion of the stored energies
between the at least first and second energy storage means; and

clock signal generating means for generating the clock
signals, said clock signal generating means simultaneously keeping
all of the clock signals in holding states during a holding period
15 during operation, said holding states being equal to the states of
the respective clock signals immediately before the holding
stateperiod.

18. (Previously Presented) The electronic circuit as claimed in claim 17, wherein the switching means and the at least first and

second energy storage means are implemented with the use of at least one charge pump.

19. (Previously Presented) A medium for storing/reading of user information, comprising an integrated circuit comprising the electronic circuit as defined in claim 17.

20. (Previously Presented) The medium as claimed in claim 19, wherein the integrated circuit comprises a photosensitive sensor for providing the input voltage when the photosensitive sensor receives a substantial quantity of light.

21. (Previously Presented) The medium as claimed in claim 20, wherein the integrated circuit furthermore comprises memory means provided with a supply voltage through utilization of the output voltage.

22. (Previously Presented) The medium as claimed in claim 21, wherein the integrated circuit further comprises a microprocessor and a further photosensitive sensor for providing additional information to the microprocessor, said microprocessor processing
5 the additional information, and said microprocessor being coupled to the memory means for storing the processed additional information.

23. (Previously Presented) The medium as claimed in claim 21,
wherein the integrated circuit further comprises a microprocessor
and a further photosensitive sensor for providing additional
information to the memory means for storing the additional
5 information, the microprocessor being coupled to the memory means
for processing the additional information after reading of the
additional information from the memory means.

24. (Currently Amended) The medium as claimed in claim 22,
wherein the length of the holding period corresponds by
approximation substantially to that of a time period during which
the photosensitive sensor does not receive a ~~substantial~~ quantity
5 of light sufficient to provide the input voltage.

25. (Currently Amended) The medium as claimed in claim 24,
wherein the microprocessor is idle during the holding period, and
the integrated circuit further comprises a standby circuit for
supplying the microprocessor with a supply voltage during the
5 holding period ~~-(R_{up})~~.

26. (Previously Presented) The medium as claimed in claim 19,
wherein the medium is an optical disc having a side for storing and
reading the user information, wherein the integrated circuit is
fastened to said side of the optical disc in a region not reserved
5 for storing and reading of the user information.

27. (Previously Presented) The medium as claimed in claim 19, wherein the medium is an optical disc having a first side for storing and reading of the user information, wherein the integrated circuit is fastened to a second side of the optical disc.

28. (Previously Presented) A recording/playback device comprising means for storing/reading of information onto/from the medium as claimed in claim 19.